

09/29/00
Jc923 U.S. PTO

PATENT APPLICATION TRANSMITTAL LETTER
(Large Entity)

Docket No.
71493-897

TO THE ASSISTANT COMMISSIONER FOR PATENTS

Transmitted herewith for filing under 35 U.S.C. 111 and 37 C.F.R. 1.53 is the patent application of:

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For: **SYSTEM AND METHOD FOR LAYER 2 FRAME DELINEATION RECOVERY**

Enclosed are:

- ☐ Certificate of Mailing with Express Mail Mailing Label No.
- ☒ 7 sheets of drawings.
- ☐ A certified copy of a application.
- ☒ Declaration ☐ Signed. ☒ Unsigned.
- ☒ Power of Attorney
- ☐ Information Disclosure Statement
- ☐ Preliminary Amendment
- ☒ Other: **AR Card**

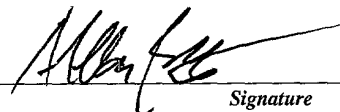
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CLAIMS AS FILED

For	#Filed	#Allowed	#Extra	Rate	Fee
Total Claims	31	- 20 =	11	x \$18.00	\$198.00
Indep. Claims	4	- 3 =	1	x \$78.00	\$78.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
BASIC FEE					\$690.00
TOTAL FILING FEE					\$966.00

- ☒ A check in the amount of **\$966.00** to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. **19-2550** as described below. A duplicate copy of this sheet is enclosed.
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Dated: **September 28, 2000**


Signature

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PATENT TRADEMARK OFFICE

CC:

SYSTEM AND METHOD FOR LAYER 2 FRAME DELINEATION RECOVERY

Field of the Invention

The invention relates to methods and systems for layer 2 frame delineation recovery, for example for delineation recovery for MAC frames transmitted over OFDM (Orthogonal Frequency Division Multiplexing) symbols.

Background of the Invention

In OFDM based transmission systems, the number of data bits carried by one OFDM symbol (a type of layer 1 frame) tends to be very large compared to that of conventional low speed transmission systems. For example, if one OFDM symbol consists of 1024 sub-carriers with QPSK modulation and rate one half channel encoding, and 80% capacity used for data bits, the number of data bits which would be transmitted for a single OFDM symbol would be about 100 bytes. If channel conditions allow, the number could be even as high as 800 bytes with the 256 QAM modulation scheme and near rate one coding. With these high-speed data transmission schemes, it is important to have an efficient data-packing scheme because even a fraction of the symbol that is not used for transmission could end up being several hundred bytes long.

It has become common to have variable length layer 2, or MAC frames, in order to support the bursting nature of data arrivals. It is desirable to have layer 2 frames which are capable of carrying complete upper layer transport units, for example IP packets. Thus, rather than forcing the layer 2 frame to be a fixed multiple of the layer 1 frame length, it has become common to allow the layer 2 frame to have a variable length thereby avoiding the potential wastage which would occur

in the event that the layer 2 frame was a fixed size and the upper layer packet does not fit into the layer 2 frame.

In a variable length layer 2 frame, the length information is located in the header of the layer 2 frame so the receiver knows the end of the frame as well as the beginning of the next frame. Disadvantageously, when the header is in error, the receiver does not know where the next frame starts and consequently will lose data in subsequent layer 2 frames until a new layer 2 frame boundary is detected. In one effort to deal with this issue, layer 1 frames are grouped in "super-frames" for example consisting of 10 layer 1 OFDM symbols. The beginning of such a super-frame is always used to start the transmission of a new layer 2 frame. Thus if a header is in error and the receiver is unable to determine the next layer 2 frame boundary, the receiver will always know that there will definitely be a layer 2 frame boundary at the start of the next super-frame. Unfortunately, this can result in a substantial loss of data.

An example of this is shown in Figure 1. In Figure 1 a sequence of layer 1 OFDM symbols is indicated by 10. Two scheduling intervals 11 and 12 are shown, these being equivalent to the above referenced super-frames. A MAC frame 12 starts at the beginning of the first scheduling interval 11, and similarly a MAC frame 18 starts at the beginning of the second scheduling interval 12. Scheduling interval 11 also includes MAC frames 14 and 16 transmitted in sequence after MAC frame 12. MAC frames 12, 14 and 16 have respective headers 13, 15 and 17. Similarly, during the second scheduling interval 12, MAC frames 20, 22, 24 and 26 follow MAC frame 18, and the MAC frames 18, 20, 22, 24 and 26 have respective headers 19, 21, 23, 25 and 27. In the event during reception over a

scheduling interval 11, MAC frame 14 is received with an error in its header 15, the system is unable to determine the length of MAC frame 14 and as such does not know where the start of the next MAC frame 16 will be. For this reason the entirety of
5 MAC frames 14 and 16 will be lost, and the receiver can only start decoding MAC frames again beginning with MAC frame 18 because it knows that a new scheduling interval 12 begins there because that is the start of a super-frame. A similar loss is shown during scheduling interval 12 in which there is shown to
10 be an error in the header 21 of MAC frame 20 resulting in the loss of the entirety of MAC frames 20, 22, 24 and 26. In OFDM based very high data rate transmission systems, these types of loss can result in severe performance degradations since the number of data bits in one symbol is large and each header
15 carries other sensitive control information.

Another existing method of trying to deal with this issue is to include a predetermined bit pattern as a frame start and frame end indication. Such start and end indicators may require more bits than the original header information, and
20 are also subject to error.

Summary of the Invention

The invention provides methods, receivers and transmitters all adapted to assist in the performance of layer 2 frame delineation. The invention is applicable in the
25 context of OFDM, but is also applicable to any layer 1 design facilitating high data rate transmission. Advantageously, the invention allows the identification of where a layer 2 frame begins notwithstanding the fact that the previous layer 2 header, which would normally be used to identify the location
30 of the next layer 2 frame, has been received in error. This means that rather than discarding a lot of data after an

erroneous header, for example all data until a super-frame boundary, layer 2 frames can be correctly received starting with the next header received in a non-erroneous layer 1 frame.

5 The method implemented at a transmitter, provided by one broad aspect of the invention involves prior to transmitting layer 1 frames, adding layer 2 boundary information to each layer 1 frame, the layer 2 boundary information indicating whether there is a layer 2 header within the layer 1 frame and indicating where in the layer 1 frame the layer 2 header begins. The layer 2 boundary information can consist of frame delineation bits added to each layer 1 frame.

10 In one embodiment, the transmitter is required to align the start of each layer 2 frame with one of a number of predetermined layer 2 start positions within layer 1 frames. The boundary information is then used to indicate which one of the possible start positions is the start position of each frame.

15 In some embodiments, the boundary information is transmitted on dedicated OFDM sub-carriers or some other dedicated channel. In another embodiment, the boundary information is transmitted in punctured symbol locations within the layer 1 frames.

20 Another broad aspect provides a transmitter adapted to perform the above described transmit frame delineation functionality. More particularly, this provides layer 1 functionality adapted to, prior to transmitting layer 1 frames, add layer 2 boundary information indicating whether there is a layer 2 header within the layer 1 frame, and to indicate where in the layer 1 frame the layer 2 header begins.

According to another broad aspect, the invention provides the functionality executed by a receiver of frames transmitted using the above noted transmit functionality. The receiver receives a sequence of layer 1 frames. For each layer 5 1 frame received, the boundary information is extracted from the layer 1 frame indicating whether there is a layer 2 header within the layer 1 frame, and indicating where in the layer 1 frame the layer 2 header begins.

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10 A determination is made of whether the layer 1 frame is received in error. If so, the layer 1 frames are not passed up to layer 2 until a layer 1 frame is received without error with boundary information indicating a layer 2 header is located within the layer 1 frame.

15 On an ongoing basis, layer 2 headers are examined to determine the location of the next layer 2 header. When the delineation procedure is required due to an error in the header, the receiver looks for a layer 2 header in a location identified by the boundary information.

20 Another broad aspect provides a receiver adapted to implement methods such as described above. Further aspects provide a computer usable medium containing instructions for causing processing hardware to execute the above described methods.

Brief Description of the Drawings

25 Preferred embodiments of the invention will now be described with reference to the attached drawings in which:

Figure 1 is an example of layer 2 frames being transmitted over layer 1 frames in super-frames, illustrating how multiple layer 2 frames may be lost in conventional systems due to an error in a single header;

5 Figure 2 shows the layer 1 and layer 2 frames of Figure 1 to which a frame delineation recovery scheme according to the invention has been applied;

10 Figure 3 is a block diagram of a receiver and transmitter adapted to perform layer 2 frame delineation according to an embodiment of the invention;

 Figure 4 is a flowchart of functionality executed by the frame delineation recovery block of Figure 3;

 Figure 5 is a flowchart of layer 2 header processing;

15 Figure 6 shows an example format for the frame delineation bits; and

 Figure 7 is a detailed example of frames and OFDM symbols which feature the frame delineation bits in accordance with the example at Figure 6.

Detailed Description of the Preferred Embodiments

20 Referring now to Figure 2, shown is the same sequence of layer 1 OFDM symbols 10 and MAC frames discussed previously with respect to Figure 1. In this example, a frame delineation recovery method has been implemented as will be described in detail below such that notwithstanding the error in the header
25 of 15 of packet 14, the location of the header 17 of the subsequent frame 16 can be determined, and since that OFDM symbol was not received in error that frame can be received thereby reducing the amount of lost data to consist solely of

MAC frame 14. Applying the same method to the subsequent scheduling interval 12 results in only MAC frame 20 being lost instead of MAC frames 20, 22, 24 and 26 as previously was the case in Figure 1.

5 Referring now to Figure 3, details of an example transmitter and receiver adapted to implement the frame delineation recovery will be described. Shown is transmitter functionality generally indicated by 40 connected through a wireless channel 44 to receiver functionality generally
10 indicated by 42. Layer 2 functionality is generally indicated by 46 for the transmitter 40 and 70 for the receiver 42. The transmit layer 2 functionality 46 generates layer 2 data 48 and layer 2 boundary information 50. These are passed to
15 respective coding/modulation blocks 52, 54. The boundary information in this embodiment will be implemented as frame delineation (FD) bits. A detailed example of the FD bits is presented below. The FD bits identify if, and where, a new MAC frame begins within the layer 1 OFDM symbol. In some
20 embodiments, the coding and modulation applied to the frame delineation bits by coding/modulation block 54 is stronger than that applied to the data by coding/modulation block 52 such that there is a higher probability that at the receiver 42 the frame delineation bits are detected correctly. Data output by the two coding/modulation blocks 52,54 is passed to an OFDM
25 transmitter 56 which puts the modulated and encoded data and FD bits onto OFDM sub-carriers and transmits OFDM symbols over the channel 44. In some embodiments, the frame delineation bits are transmitted on a separate sub-carrier or sub-carriers to the data information.

30 The receiving functionality 42 includes an OFDM receiver 58 which receives the layer 1 OFDM symbols and passes

the signals from the sub-carrier used for the frame delineation bits to a first decoding/demodulation block 60, and passes the signals from the remaining sub-carriers used for data to a second decoding/demodulation block 62. The

5 decoding/demodulation block 60 performs decoding and demodulation on the sub-carrier containing the frame delineation bits and passes these frame delineation bits to a MAC frame boundary detection block 64. This block processes the boundary information such as frame delineation bits to
10 determine whether or not a new MAC frame begins in that layer 1 OFDM symbol, and if so where in the symbol the new MAC frame begins. This information 69 is passed to a MAC frame delineation recovery block 68. The decoding and demodulation block 62 performs decoding and demodulation on the remaining
15 sub-carriers and passes the data thus produced to an error detection block 66 which determines whether the symbol was received in error for example by performing a CRC check. The error detection block 66 produces an output 67 to the MAC frame delineation recovery block 68 which indicates whether or not
20 the OFDM symbol was received in error. The MAC frame delineation recovery block 68 uses the information received from the MAC frame boundary detection block 64 and the error detection block 66 to decide either to discard the layer 1 OFDM symbol all together, or to pass it up to layer 2 functionality
25 70. It is noted that the error detection function can be considered a function performed between layer 1 and layer 2, a function performed at layer 1 or a function performed at layer 2. This simply depends upon where the function is implemented.

The functionality implemented by the MAC frame
30 delineation recovery block 68 will now be described with reference to Figure 4.

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The functionality begins with the receipt and decoding of a layer 1 OFDM symbol (step 4-1). The error detection block 66 produces the output 67 indicating whether or not the symbol was received in error. If the symbol was not in error (No path, step 4-2) then in step 4-3 the symbol data is passed up to layer 2 functionality 70. If on the other hand, the error detection block does indicate that the symbol was received in error (Yes path, step 4-2) then at step 4-4 if the erroneous symbol does not contain a header (no path, step 4-4, known from previously correctly decoded header) then the process returns to step 4-1 and the receipt and decoding of the next OFDM symbol. On the other hand, the frame delineation recovery step, generally indicated by step 4-5 is executed in the event the erroneous symbol did in fact contain a header. (yes path, step 4-4) Step 4-5 is a loop which is now repeated until a symbol is received which is not in error and which contains a MAC header. This begins with the receipt and decoding of a layer 1 OFDM symbol at step 4-6. In step 4-7, if that symbol was received in error (Yes path) then that is the end of that iteration of the loop and the loop continues back at step 4-6 with the receipt of another symbol. On the other hand, if the symbol was not received in error (No path) then the MAC frame boundary information received from the MAC frame boundary detection block 64 is examined to determine whether or not the symbol contains a MAC frame header. In the event there is no MAC frame header, (No path, step 4-8) then further information is still being received for a MAC frame for which the header was received in error. As such, there is no purpose for this data and that iteration of the loop ends and the process continues with the receipt of another layer 1 OFDM symbol at step 4-6. On the other hand, if the symbol does contain a MAC frame header (Yes path, step 4-8) then the MAC frame header position determined from the frame boundary

information received from the MAC frame boundary detection block 64 is passed up to layer 2 70 at step 4-9. Following this, the symbol data is also passed up to layer 2 at step 4-3. Whenever an error occurs in the symbol the entire symbol may be
5 discarded. It is also possible that portions of the erroneous symbol may be of use and these can be passed up to layer 2.

On the transmit side 40 MAC frame boundary information 50 can be generated at any appropriate point in the processing. This can be done at layer 2 or layer 1 for
10 example.

On the receive side 70 a modified layer 2 frame reception method is required. Referring now to Figure 5, a flowchart of layer 2 frame header processing is shown. The details of processing of the data portion of the layer 2
15 processing are not shown as these can be implemented in any conventional manner. The method starts with layer 2 correctly receiving a layer 2 header in a known location (step 5-1) either because of a super-frame mechanism, because layer 1 indicates where it is using boundary information, or through
20 some other start-up procedure. That header is decoded to determine the location of the header for the next layer 2 frame by examining the length field (step 5-2). Now, some time later, layer 2 will look in the expected location for the next header. If that header is correctly received (Yes path, step
25 5-3), then that iteration of header processing ends, and steps 5-2, 5-3 are repeated until a header is not found in the expected location (No path, step 5-3) due to layer 1 symbol in error. Then layer 2 must wait until it receives boundary information from layer 1 indicating the location of a layer 2
30 header (step 5-4). When such boundary information is received,

the process flow returns to step 5-2 which again determines an expected location for the next layer 2 header.

An example implementation of the frame delineation bits will now be described with reference to Figure 6. In this example it is assumed that there are four bits included in each OFDM symbol which indicate firstly whether there is a MAC frame boundary within the symbol and secondly where that MAC frame boundary occurs. Referring now to Figure 6, the first bit 80 indicates whether the symbol contains a MAC frame boundary in accordance with the legend of Table 81. In this example, zero indicates that there is no new frame while a one indicates that there is a new frame. The following three bits 82 are used to indicate the start position of the new frame. These bits indicate to a resolution of 1/8 of an OFDM symbol the position of the frame running from 000 indicating that the frame starts right at the beginning of the symbol through to 111 indicating that the frame starts 7/8ths of the way through the symbol. This assumes that the MAC layer is constrained to start new frames only at positions which are 1/8th multiples of OFDM symbols in length.

Turning now to Figure 7, a specific example in which the frame delineation bit format described with respect to Figure 6 has been employed will now be described. In this example there are MAC frames, MAC frame A 90, MAC frame B 91, and MAC frame C 92 which are being transmitted over layer 1 symbols three of which are indicated as symbol one 94, symbol two 96 and symbol three 98. It can be seen that symbol one will contain data from MAC frame A 90 entirely, and that there is no MAC frame boundary. As such, the first bit in the frame delineation bits 95 will be a zero indicating no new frame, and the content of the remaining three bits is not relevant.

Symbol two 96 on the other hand includes data from MAC frame A 90 and MAC frame B 91 and as such does include a MAC frame boundary. To indicate this the frame delineation bits 97 include a first bit which is a one indicating a new frame.

5 Furthermore, the remaining three bits are set to "101" in this example indicating that the new MAC frame starts at a point 5/8ths of the way through the layer 1 symbol. Similarly, symbol three 98 includes data from both MAC frame B 91 and MAC frame C 92 and as such the frame delineation bits 99 include a
10 first frame delineation bit set to one indicating that there is a new frame. The remaining three bits in this case are set to "100" indicating that the new frame begins at a point 1/2 of the way through the layer 1 symbol. In this example, if the header of MAC frame A 90 is received in error, OFDM symbol one
15 is discarded, the data for MAC frame A in symbol two is discarded but it is possible to determine from the frame delineation bits in symbol two where the beginning of MAC frame B 91 is and the portion of symbol two 96 relevant to MAC frame B 91 can be extracted. The remainder of frame B is recovered
20 from symbol three 98. What this means is that MAC frame B does not need to be retransmitted, but rather only MAC frame A 90 needs to be retransmitted. In the above example, three frame delineation bits are used to allow frame delineation to a resolution of 1/8th of an OFDM symbol boundary. Of course it
25 is to be understood that a higher or lower granularity could be achieved by using more or fewer frame delineation bits for this purpose. For example, if the granularity level is $M=2^n$, then there are M points in the OFDM symbol where a MAC frame can begin. This is a constraint which is added to the MAC layer,
30 namely that layer 2 frames can only start at one of a set of predetermined start positions. As indicated previously, the frame delineation bits may be encoded and modulated separately

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from the data traffic bits to provide more protection from channel errors. For example, the traffic data may be encoded with rate 5/6 code and modulated with 64QAM, and the frame delineation bits may be encoded with a 1/2 rate code and
5 modulated with QPSK. A small subset of sub-carriers of OFDM symbol can be reserved for the transmission of the frame delineation bits. This reservation of sub-carriers needs to be predefined and known both at the receiver and transmitter so that the receiver can decode and demodulate them separately.

10 In a variable length MAC frame, the header includes information on the length of the MAC frame. The receiver extracts the length information and decides when the next frame starts. The header should be protected by an error detection
15 code so that the length information may not be used incorrectly in case of an error in the header. When a header error is detected, the receiver looks at the frame delineation bits to find the beginning boundary of the next MAC frame. Because frame delineation bits are protected better than traffic bits, they can be correctly decoded even when the traffic bits
20 including the frame header are in error. Therefore the probability that the next MAC frame boundary is lost will be reduced.

In the above described embodiments, it has been assumed that the layer 1 frame is an OFDM symbol. More
25 generally, the invention can be applied to other layer 1 protocols which do not use OFDM symbols but support high data rates. Furthermore, the invention has been described with reference to the MAC layer 2 protocol. More generally, the invention is applicable to layer 2 frames which have frame
30 boundaries which are not aligned with the layer 1 boundaries. It is noted that the method provided by the invention to

provide layer 2 frame delineation may be used in combination with the super-frame method which enforces the layer 2 frames to be aligned with layer 1 frames periodically. Alternatively, the new frame delineation method can be used on its own without
5 the need for periodic resyncing to the layer 1 frame.

In the above described embodiments, the boundary information is transmitted using dedicated OFDM subcarriers. More generally, the boundary information can be transmitted using dedicated channels of any suitable type. Alternatively,
10 the boundary information can be transmitted in punctured symbol locations of layer 1 frames.

Embodiments of the invention can be implemented using hardware, software, or any suitable combination of hardware and software.

15 Numerous modifications and variations of the present invention are possible in light of the above teachings. It is therefore to be understood that within the scope of the appended claims, the invention may be practised otherwise than as specifically described herein.

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WE CLAIM:

1. A method of performing layer 2 frame delineation comprising:

prior to transmitting layer 1 frames, adding layer 2
5 boundary information to each layer 1 frame, the layer 2
boundary information indicating whether there is a layer 2
header within the layer 1 frame and indicating where in the
layer 1 frame the layer 2 header begins.

2. A method according to claim 1 wherein the layer 2
10 boundary information comprises frame delineation bits added to
each layer 1 frame.

3. A method according to claim 2 wherein the frame
delineation bits added to a particular layer 1 frame comprise
at least one bit indicating whether the particular frame
15 contains a layer 2 header, and at least one bit indicating
where the layer 2 header begins.

4. A method according to claim 1 further comprising:
aligning a start of each layer 2 frame with one of a
plurality of predetermined layer 2 start positions within layer
20 1 frames;

wherein indicating where in the layer 1 frame the
layer 2 header begins comprises indicating a respective one of
the plurality of predetermined layer 2 start positions.

5. A method according to claim 4 wherein there are 2^n
25 predetermined layer 2 start positions, and wherein the frame
delineation bits comprise n bits for indicating one of these
predetermined start positions.

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6. A method according to claim 1 wherein the layer 1 frames are OFDM (orthogonal frequency division multiplexing) symbols.

7. A method according to claim 6 wherein the boundary
5 information is transmitted on dedicated OFDM sub-carriers.

8. A method according to claim 1 wherein the boundary information is transmitted on a dedicated channel.

9. A method according to claim 1 wherein the boundary
10 information is transmitted in punctured symbol locations within the layer 1 frame.

10. A method according to claim 6 wherein the boundary information is transmitted in punctured symbol locations within the layer 1 frame.

11. A transmitter comprising:

15 layer 1 functionality adapted to, prior to transmitting layer 1 frames, add layer 2 boundary information indicating whether there is a layer 2 header within the layer 1 frame, and to indicate where in the layer 1 frame the layer 2 header begins.

20 12. A transmitter according to claim 11 wherein the layer 2 boundary information comprises frame delineation bits added to each layer 1 frame.

13. A transmitter according to claim 12 wherein the frame
25 delineation bits added to a particular layer 1 frame comprise at least one bit indicating whether the particular frame contains a layer 2 header, and at least one bit indicating where the layer 2 header begins.

14. A transmitter according to claim 11 further comprising:

layer 2 functionality adapted to align a header of each layer 2 frame with one of a plurality of predetermined
5 layer 2 start positions within layer 1 frames;

wherein indicating where in the layer 1 frame the layer 2 header begins comprises indicating a respective one of the plurality of predetermined layer 2 start positions.

15. A transmitter according to claim 11 wherein the layer
10 1 frames are OFDM (orthogonal frequency division multiplexing) symbols.

16. A method of performing layer 2 frame delineation comprising:

for each layer 1 frame of a sequence of layer 1
15 frames:

receiving the layer 1 frame;

extracting boundary information from the layer 1 frame indicating whether there is a layer 2 header within the layer 1 frame, and indicating where in the layer 1
20 frame the layer 2 header begins.

17. A method according to claim 16 further comprising:

for each layer 1 frame determining if the layer 1 frame is received in error;

wherein after receiving a layer 1 frame in error,
25 layer 1 frames are not passed up to layer 2 until a layer 1 frame is received without error with boundary information

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indicating a layer 2 header is located within the layer 1 frame.

18. A method according to claim 17 further comprising:

5 looking for a layer 2 header in a location identified by the boundary information.

19. A method according to claim 18 further comprising passing the boundary information up to layer 2.

10 20. A method according to claim 16 wherein the layer 2 boundary information comprises frame delineation bits added to each layer 1 frame.

15 21. A method according to claim 20 wherein the frame delineation bits added to a particular layer 1 frame comprise at least one bit indicating whether the particular frame contains a layer 2 header, and at least one bit indicating where the layer 2 frame begins.

22. A method according to claim 16 further comprising:
looking for a layer 2 header aligned with one of a plurality of predetermined layer 2 start positions within layer 1 frames;

20 wherein the boundary information indicates where in the layer 1 frame the layer 2 header begins by indicating a respective one of the plurality of predetermined layer 2 start positions.

25 23. A method according to claim 16 wherein the layer 1 frames are OFDM (orthogonal frequency division multiplexing) symbols.

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24. A receiver comprising:

layer 1 error detection function adapted to determine if a received layer 1 frame is in error, and if so to discard the received layer 1 frame;

5 boundary information processing function adapted to extract from each layer 1 frame boundary information indicating whether there is a layer 2 header located within the layer 1 frame and where the layer 2 header is located;

10 wherein after receiving a layer 1 frame in error, layer 1 frames are not passed up to layer 2 until a layer 1 frame is received without error which contains boundary information indicating a layer 2 header is located within the layer 1 frame.

25. A receiver according to claim 24 further comprising:

15 layer 2 functionality adapted to look for a layer 2 header in a location identified by the boundary information.

26. A receiver according to claim 25 wherein the layer 2 boundary information comprises frame delineation bits added to each layer 1 frame.

20 27. A receiver according to claim 26 wherein the frame delineation bits added to a particular layer 1 frame comprise at least one bit indicating whether the particular frame contains a layer 2 header, and at least one bit indicating where the layer 2 frame begins.

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28. A receiver according to claim 24 further comprising:

layer 2 functionality adapted to look for a layer 2 header aligned with one of a plurality of predetermined layer 2 start positions within layer 1 frames;

5 wherein the boundary information indicates where in the layer 1 frame the layer 2 header begins by indicating a respective one of the plurality of predetermined layer 2 start positions.

29. A receiver according to claim 24 wherein the layer 1
10 frames are OFDM (orthogonal frequency division multiplexing) symbols.

30. A computer usable medium containing instructions for causing processing hardware to execute the method of claim 1.

31. A computer usable medium containing instructions for
15 causing processing hardware to execute the method of claim 16.

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Abstract of the Invention

Methods, receivers and transmitters all adapted to assist in the performance of layer 2 frame delineation are provided which are applicable in the context of OFDM, and also applicable to any layer 1 design facilitating high data rate transmission. Advantageously, the invention allows the identification of where a layer 2 frame begins notwithstanding the fact that the previous layer 2 header, which would normally be used to identify the location of the next layer 2 frame, has been received in error. This means that rather than discarding a lot of data after an erroneous header, for example all data until a super-frame boundary, layer 2 frames can be correctly received starting with the next header received in a non-erroneous layer 1 frame. The method implemented at a transmitter, provided by one broad aspect of the invention involves prior to transmitting layer 1 frames, adding layer 2 boundary information to each layer 1 frame, the layer 2 boundary information indicating whether there is a layer 2 header within the layer 1 frame and indicating where in the layer 1 frame the layer 2 header begins. The layer 2 boundary information can consist of frame delineation bits added to each layer 1 frame. At a receiver of frames transmitted using the above noted transmit functionality, the receiver receives a sequence of layer 1 frames. For each layer 1 frame received, the boundary information is extracted from the layer 1 frame indicating whether there is a layer 2 header within the layer 1 frame, and indicating where in the layer 1 frame the layer 2 header begins.

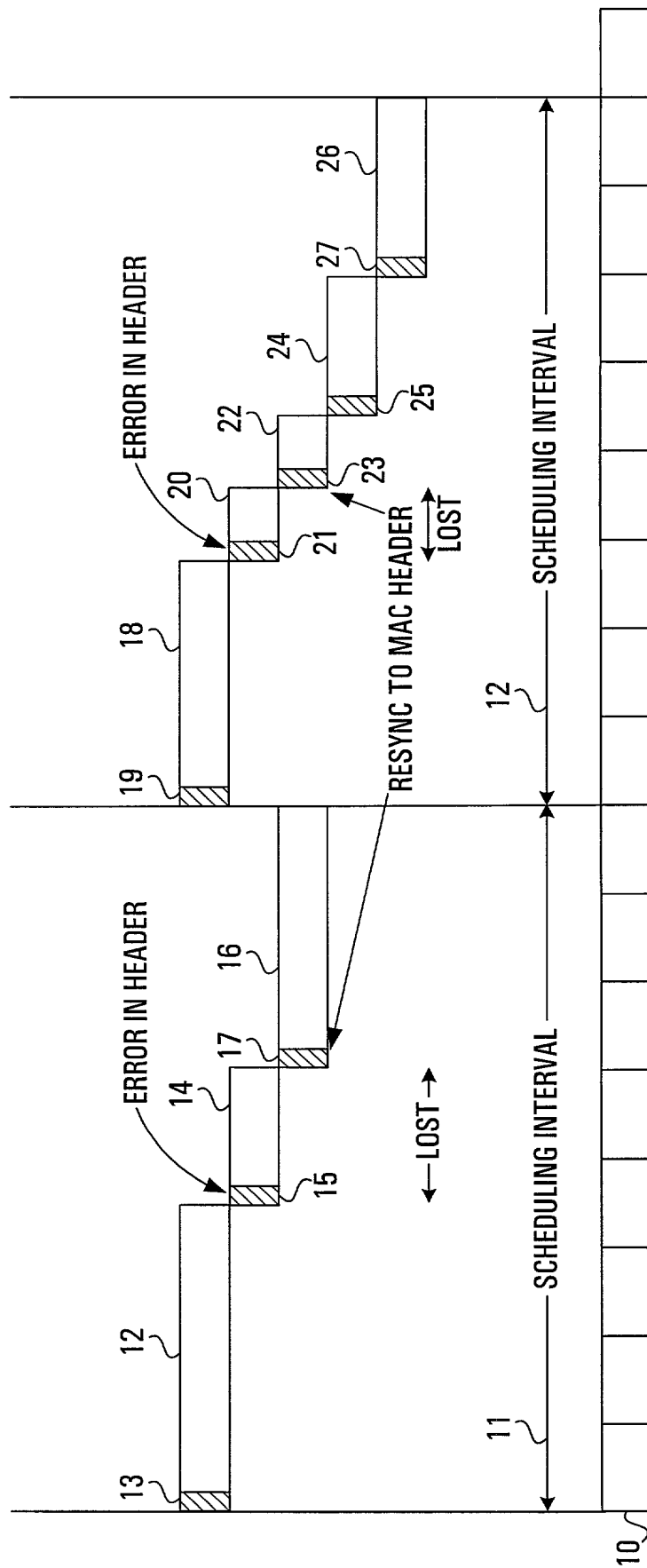


FIG. 2

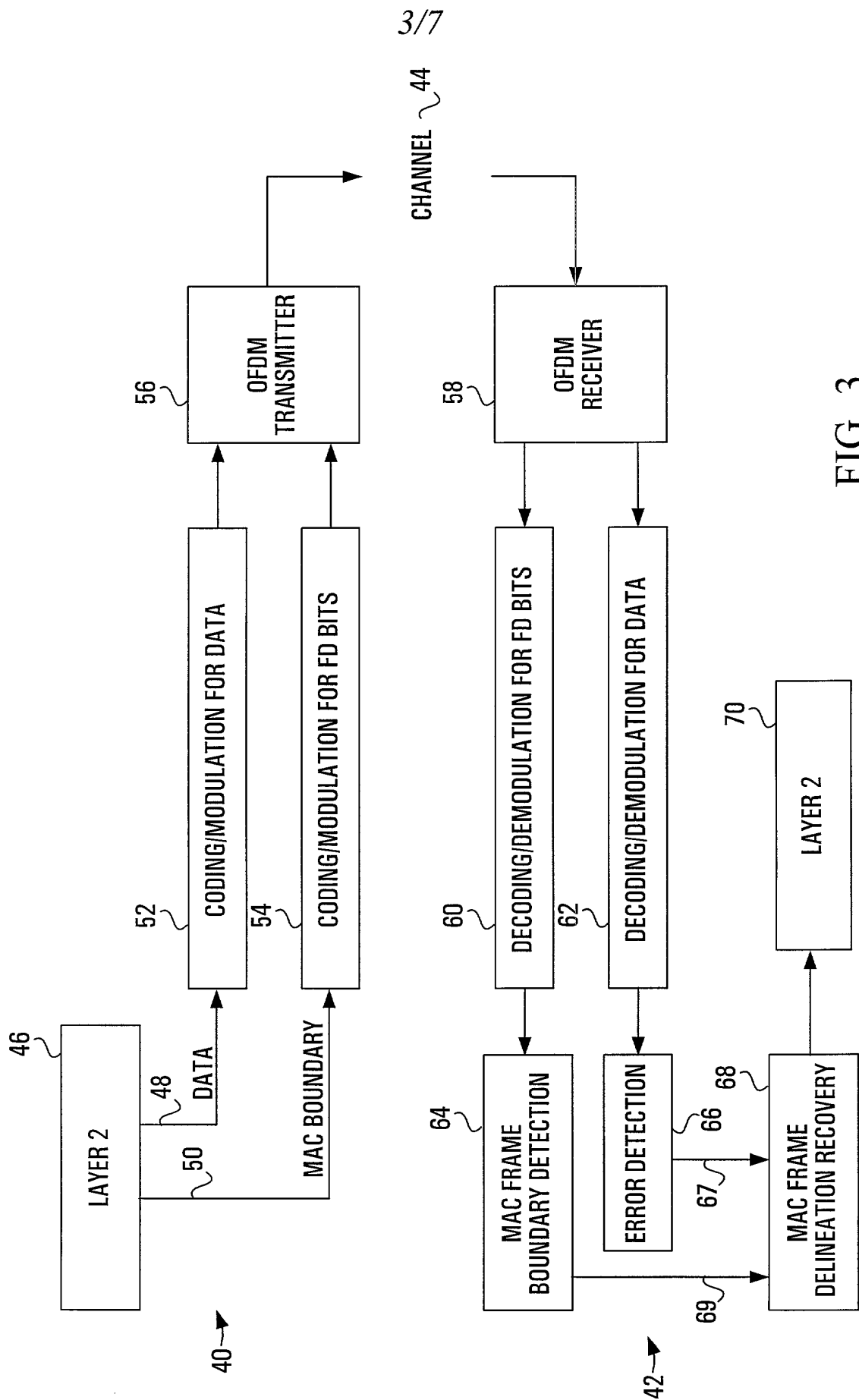


FIG. 3

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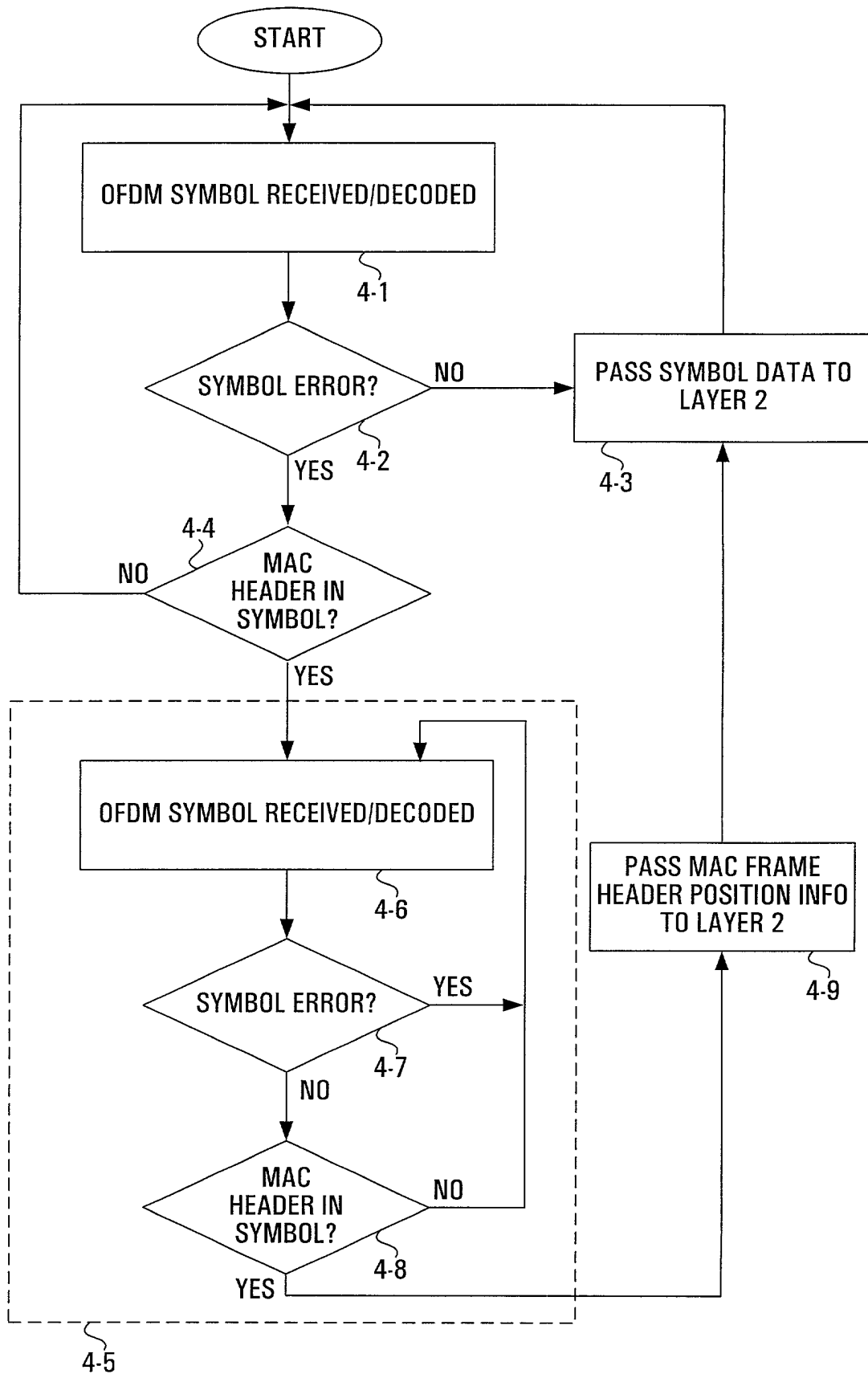


FIG. 4

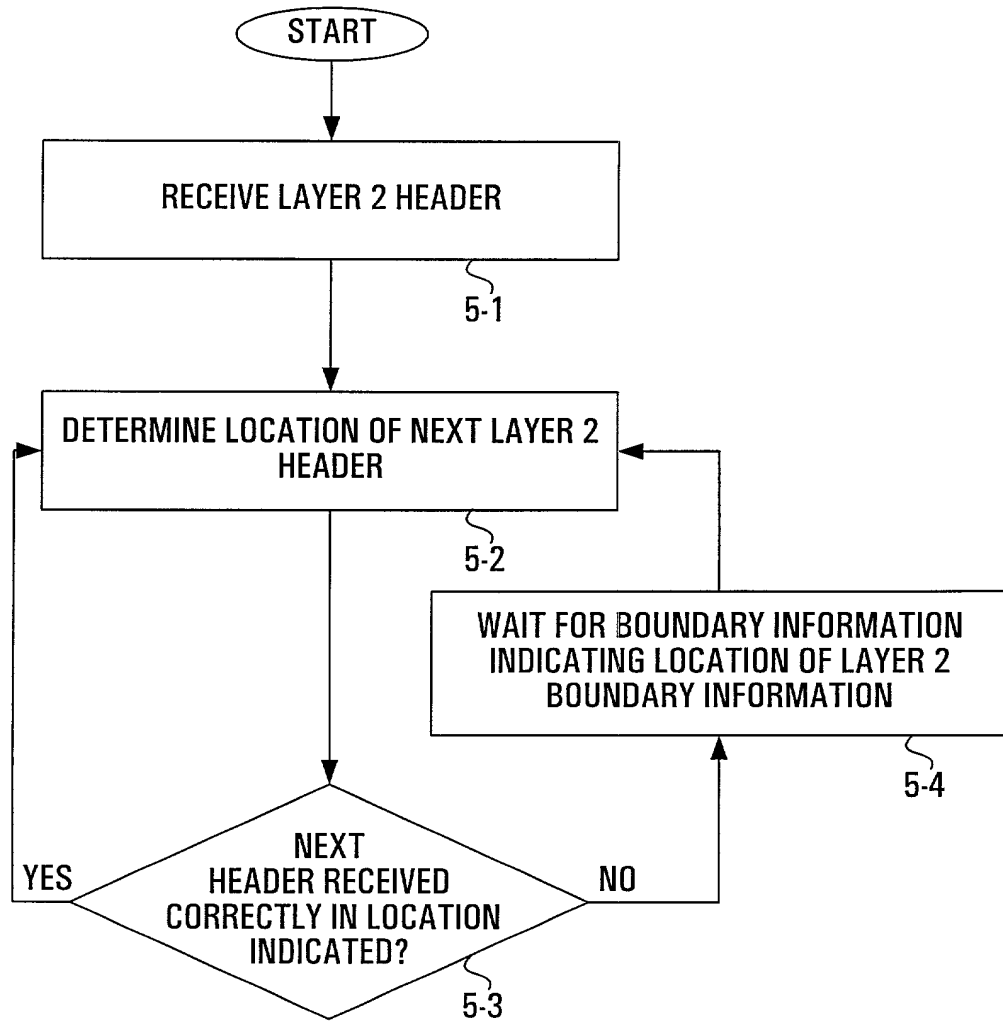


FIG. 5

POSITION OF NEW FRAME STACT WITHIN SYMBOL

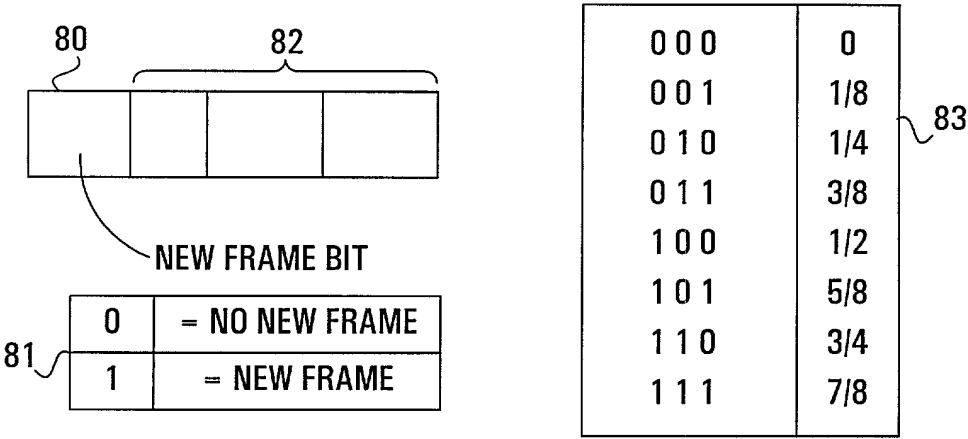


FIG. 6

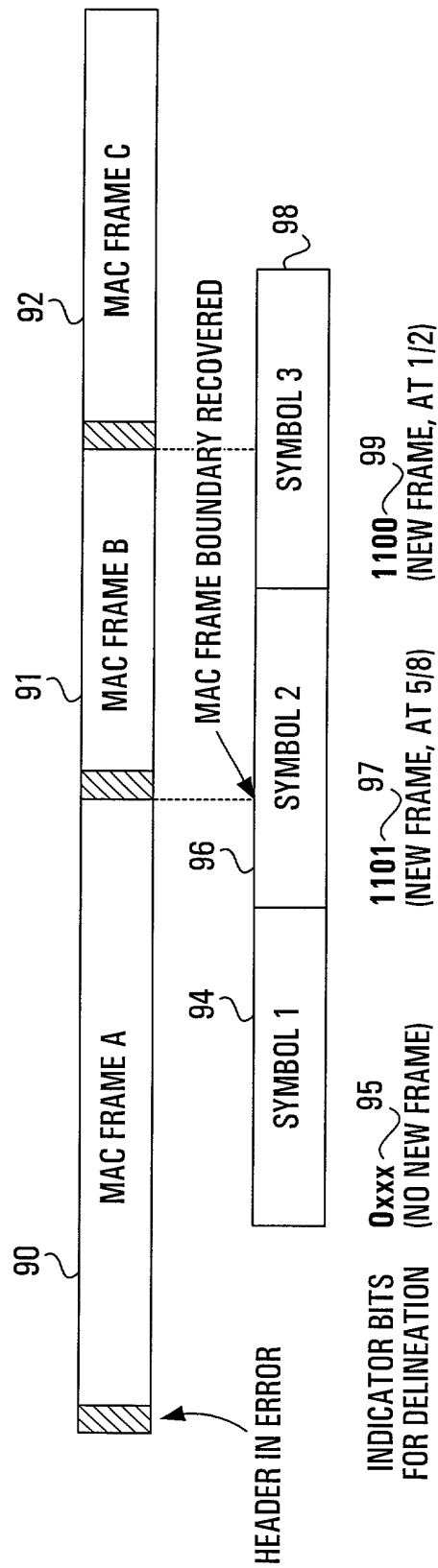


FIG. 7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

SYSTEM AND METHOD FOR LAYER 2 FRAME DELINEATION RECOVERY

the specification of which

(check one) ☒ is attached hereto.

☐ was filed on _____
as U.S. Application Serial No. _____

☐ was filed on _____
as PCT International Application No. _____

and (if applicable) was amended on _____

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information known to me which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §§1.56(a) and (b), which state:

- "(a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is cancelled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability that is cancelled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§1.97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:
- (1) prior art cited in search reports of a foreign patent office in a counterpart application,
 - (2) the closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.

- (b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and

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- (1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; or
- (2) It refutes, or is inconsistent with, a position the applicant takes in:
 - (i) Opposing an argument of unpatentability relied on by the Office, or
 - (ii) Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability."

I hereby claim foreign priority benefits under 35 United States Code, §119 and/or §365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me or my assignee disclosing the subject matter claimed in this application and having a filing date (1) before that of the application on which priority is claimed, or (2) if no priority claimed, before the filing of this application:

PRIOR FOREIGN APPLICATION(S)

<u>Number</u>	<u>Country</u>	<u>Filing Date</u> <i>(Month/Day/Year)</i>	<u>Date First</u> <u>Laid-open or</u> <u>Published</u>	<u>Date Patented</u> <u>or Granted</u>	<u>Priority Claimed?</u>
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I hereby claim the benefit under 35 United States Code, §119(e) of any United States provisional application(s) listed below:

<u>Application Number</u>	<u>Filing Date</u>
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I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

PRIOR U.S. OR PCT APPLICATION(S)

<u>Application No.</u>	<u>Filing Date</u> <i>(month/day/year)</i>	<u>Status</u> <i>(pending, abandoned, granted)</i>
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that wilful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such wilful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint the following patent agents with full power of substitution, association and revocation to prosecute this application and/or international application and to transact all business in the Patent and Trademark Office connected therewith:

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